



First Named Inventor	Frankie F. Roohparvar
Serial No.	10/786,353
Filing Date	February 25, 2004
Group Art Unit	2186
Examiner Name	Behzad Peikari
Confirmation No.	4557
Allowed	December 15, 2004
Attorney Docket No.	400.008US02
Title: SYNCHRONOUS FLASH MEMORY EMULATING THE PIN CONFIGURATION OF SDRAM	

**GENERAL
TRANSMITTAL
FORM UNDER 37 CFR 1.8
(LARGE ENTITY)**

Mail Stop: ISSUE FEE
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Enclosures

The following documents are enclosed:

- Form PTOL-85 (Issue Fee Transmittal) in duplicate (1 pg.);
- "Fee Address" Indication Form (1 pg.);
- Communication re: Comments on Examiner's Statement of Reasons for Allowance (1 pg.);
- Communication re: Submission of Replacement Formal Drawings and 33 Sheets of Replacement Drawings (1 pg.);
- A check in the amount of \$1,400.00 (Issue Fee);
- A check in the amount of \$300.00 (Publication Fee);
- A check in the amount of \$12.00 (advance order of 4 patent copies);
- An itemized return-receipt postcard

Leffert Jay & Polglaze, P.A.
P.O. Box 581009
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**Please charge any additional fees or credit any overpayments
to Deposit Account No. 501373.**

CUSTOMER NO. 27073

Submitted By

Name	Andrew C. Walseth	Reg. No.	43,234	Telephone	(612) 312-2207
Signature	<i>Andrew C. Walseth</i>			Date	<i>3/14/05</i>

Certificate of Mailing

I certify that this correspondence and the identified documents listed on this transmittal are being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop: ISSUE FEE, Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450 on March 14, 2005.

Name	Kristen A. Smith	Signature	<i>Kristen A. Smith</i>
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COMMENTS ON
EXAMINER'S STATEMENT
OF REASONS FOR
ALLOWANCE

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Applicant notes that the allowed claims define over the prior art for many different reasons. The stated reasons in the Reasons for Allowance should be interpreted as highlighting only one of the many differences between prior art and the claimed invention. Further, certain claimed limitations not discussed in the statement, particularly in dependent claims, provide additional reasons for patentability.

If the Examiner has any questions or concerns regarding this application, please contact the undersigned at (612) 312-2207.

Respectfully submitted,

Date: 3/14/05



Andrew C. Walseth

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**SUBMISSION OF
REPLACEMENT FORMAL
DRAWINGS UNDER 37
C.F.R. §1.85**

Title: SYNCHRONOUS FLASH MEMORY EMULATING THE PIN CONFIGURATION
OF SDRAM

**SUBMISSION OF REPLACEMENT FORMAL DRAWINGS
UNDER 37 C.F.R. §1.85**

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In response to the Notice of Allowance mailed December 15, 2004, attached are thirty-six (36) sheets of Replacement Formal Drawings for the above-identified patent application.

Applicant notes that, in addition to the changes requested by the Examiner to the drawings, a typographical error was also corrected in Figure 1A. In Figure 1A, a duplicate reference to element 133 was deleted. Applicant contends that no new matter was introduced as a result of this change.

Applicant believes that these drawings are in good order, and requests issuance of the Letters Patent. Please contact the undersigned attorney if you have any questions.

Respectfully submitted,

Date: 3/14/05

Andrew C. Walseth
Reg. No. 43,234

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